

FIG. 1

Prior Art

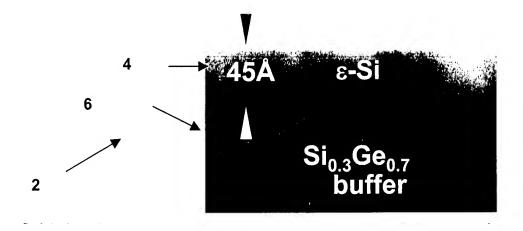


FIG. 2A

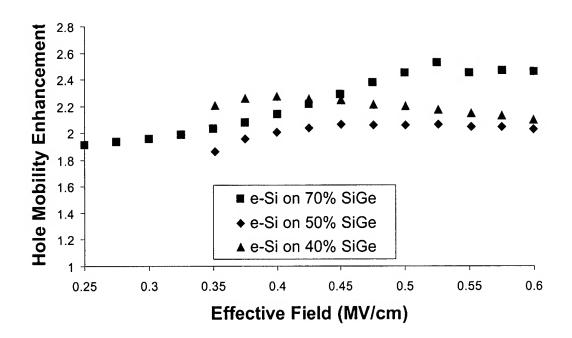


FIG. 2B

Bright layers are ε-Si Dark layers are Si_{0.3}Ge_{0.7}

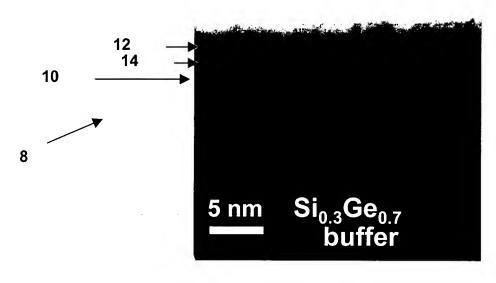


FIG. 3A

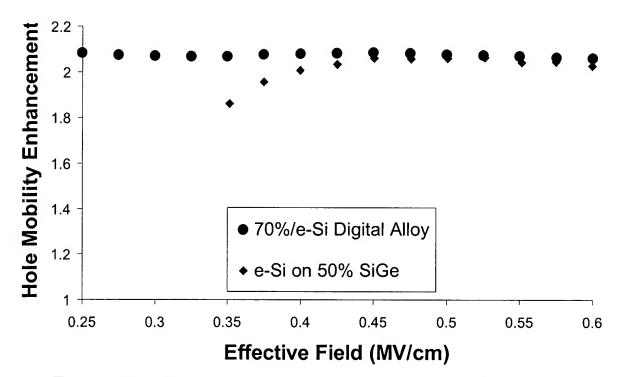


Figure 3b- Mobility enhancement of ϵ -Si/Si $_{0.3}$ Ge $_{0.7}$ digital alloy pMOSFET compared to conventional ϵ -Si device